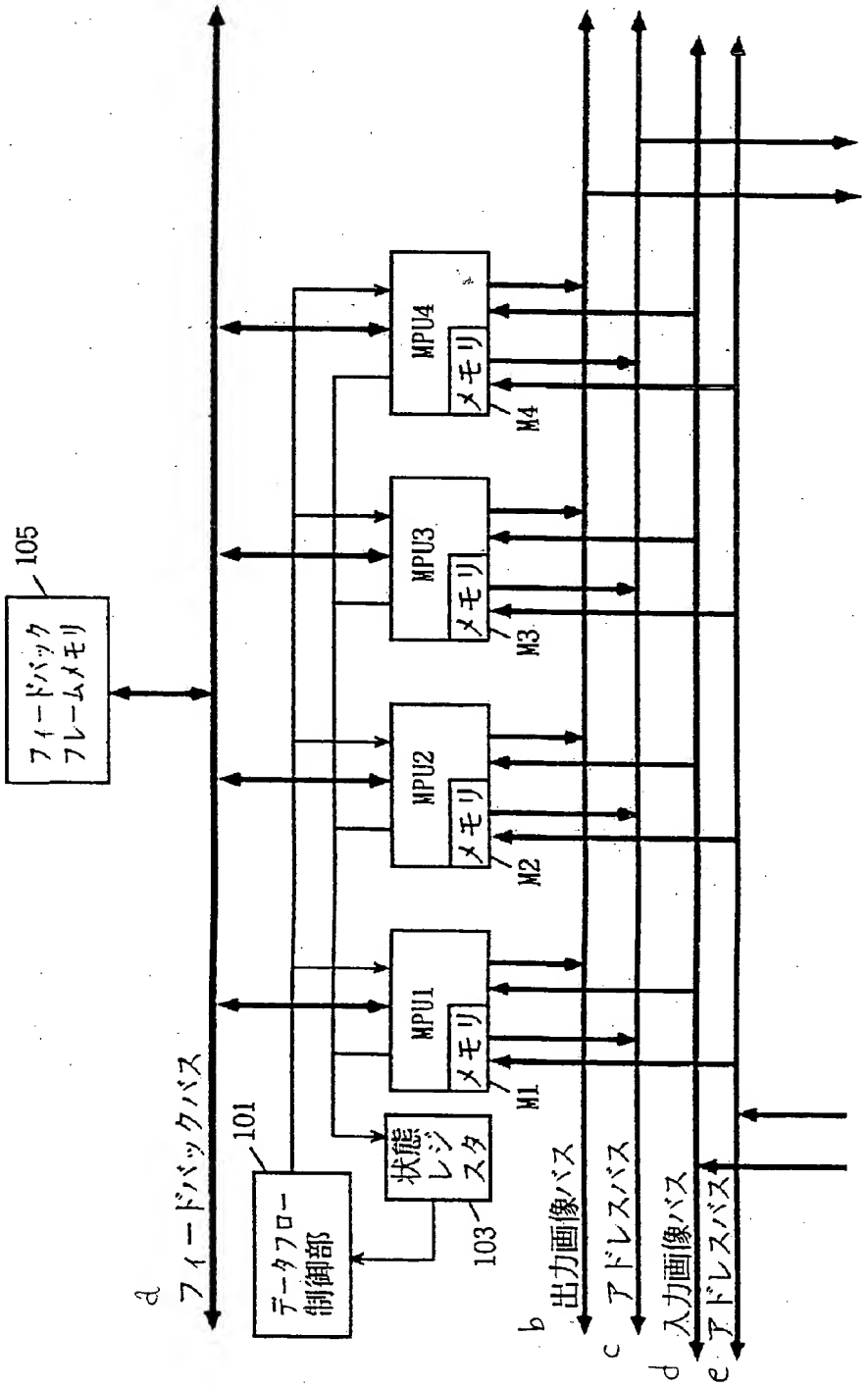
【書類名】 図面 document name drawings

【図１】 Fig. 1 (see attached sheet)



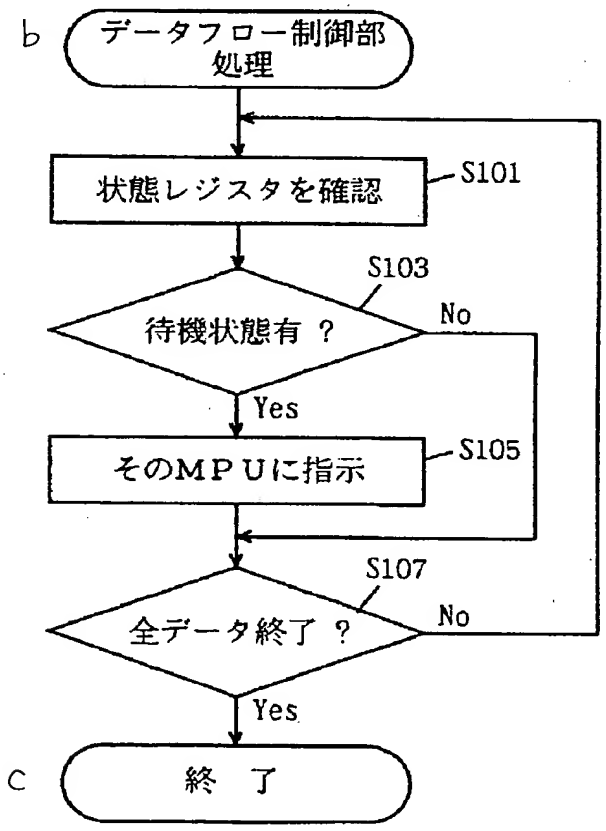
【図２】 Fig. 2 (see attached sheet)

a
状態レジスタ例

	b 待機	c 入力	d 処理	e 処理終了	f 出力
MPU1	0	1	0	0	0
MPU2	1	0	0	0	0
MPU3	0	0	1	0	0
MPU4	0	0	0	0	1

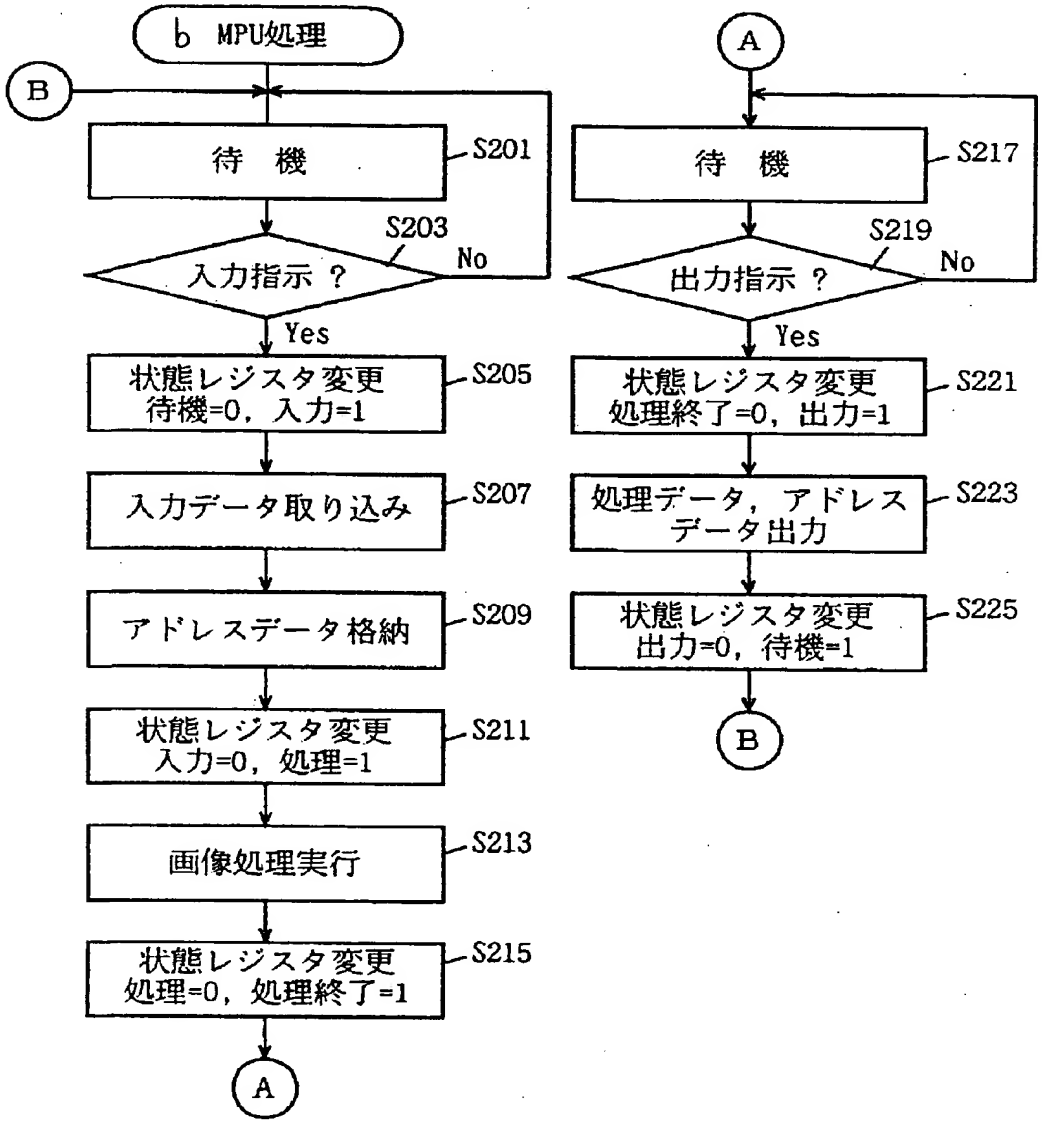
【図３】 Fig. 3 (see attached sheet)

a データフロー制御部処理フロー

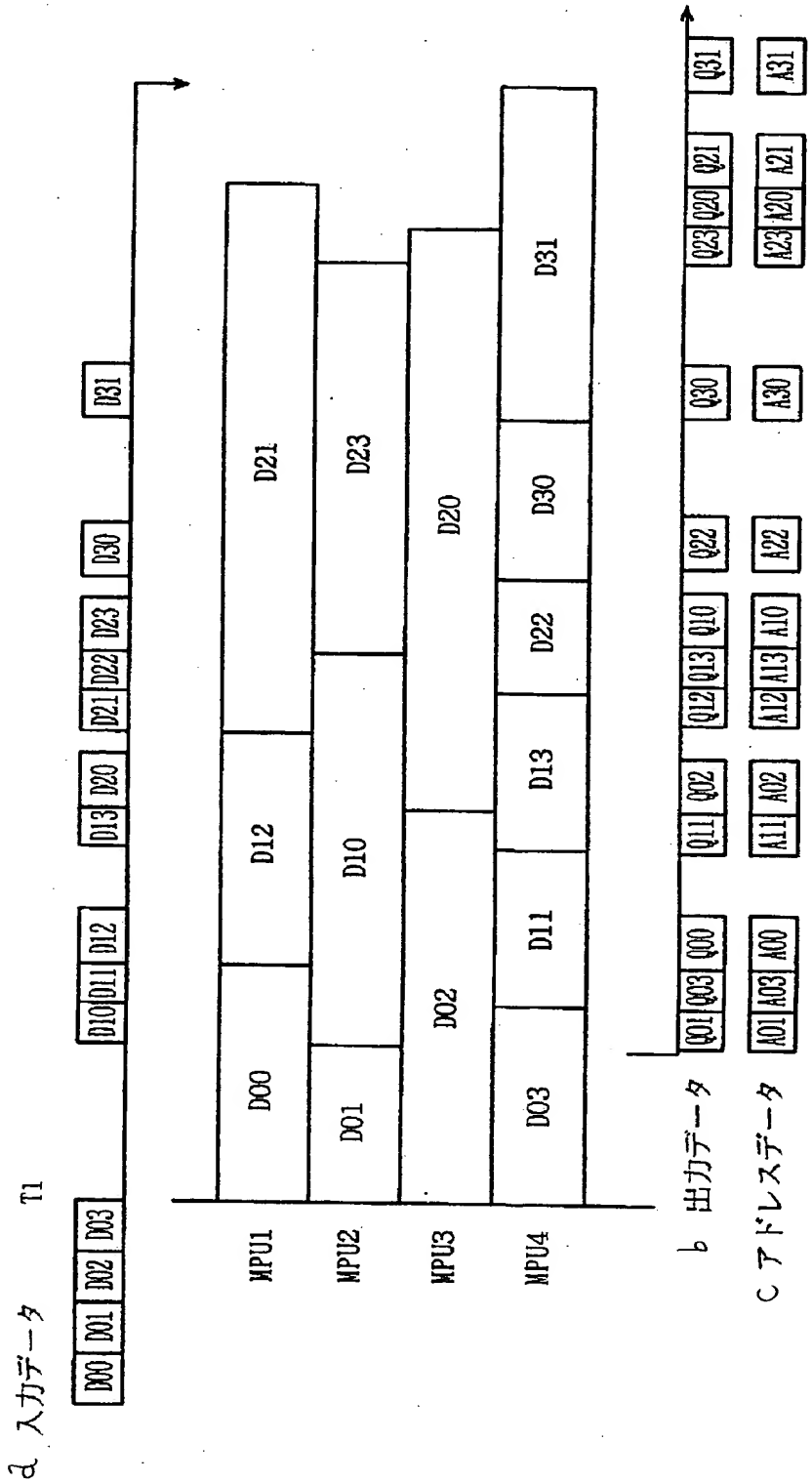


【図４】　Fig. 4 (see attached sheet)

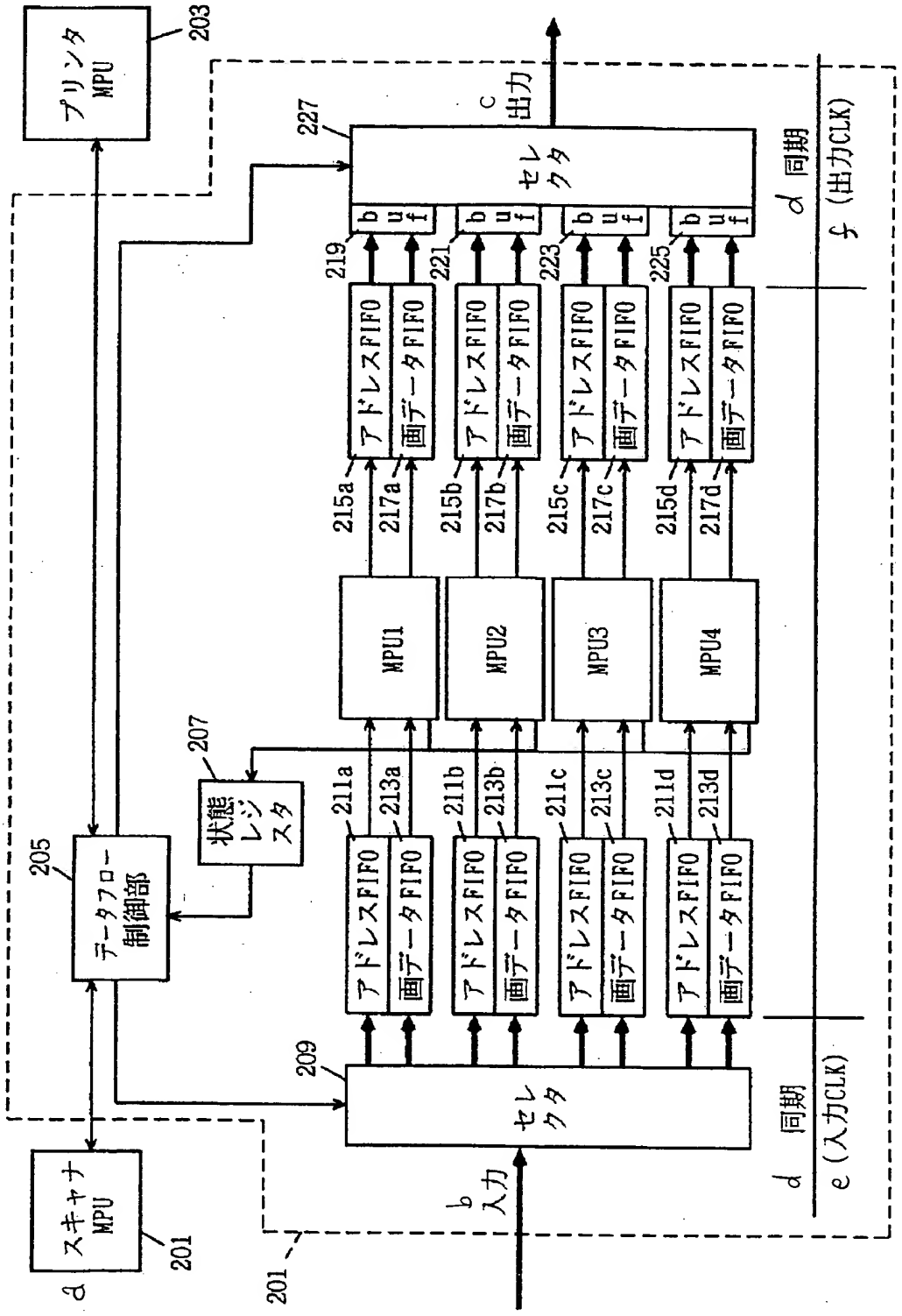
ａ ＭＰＵ処理フロー



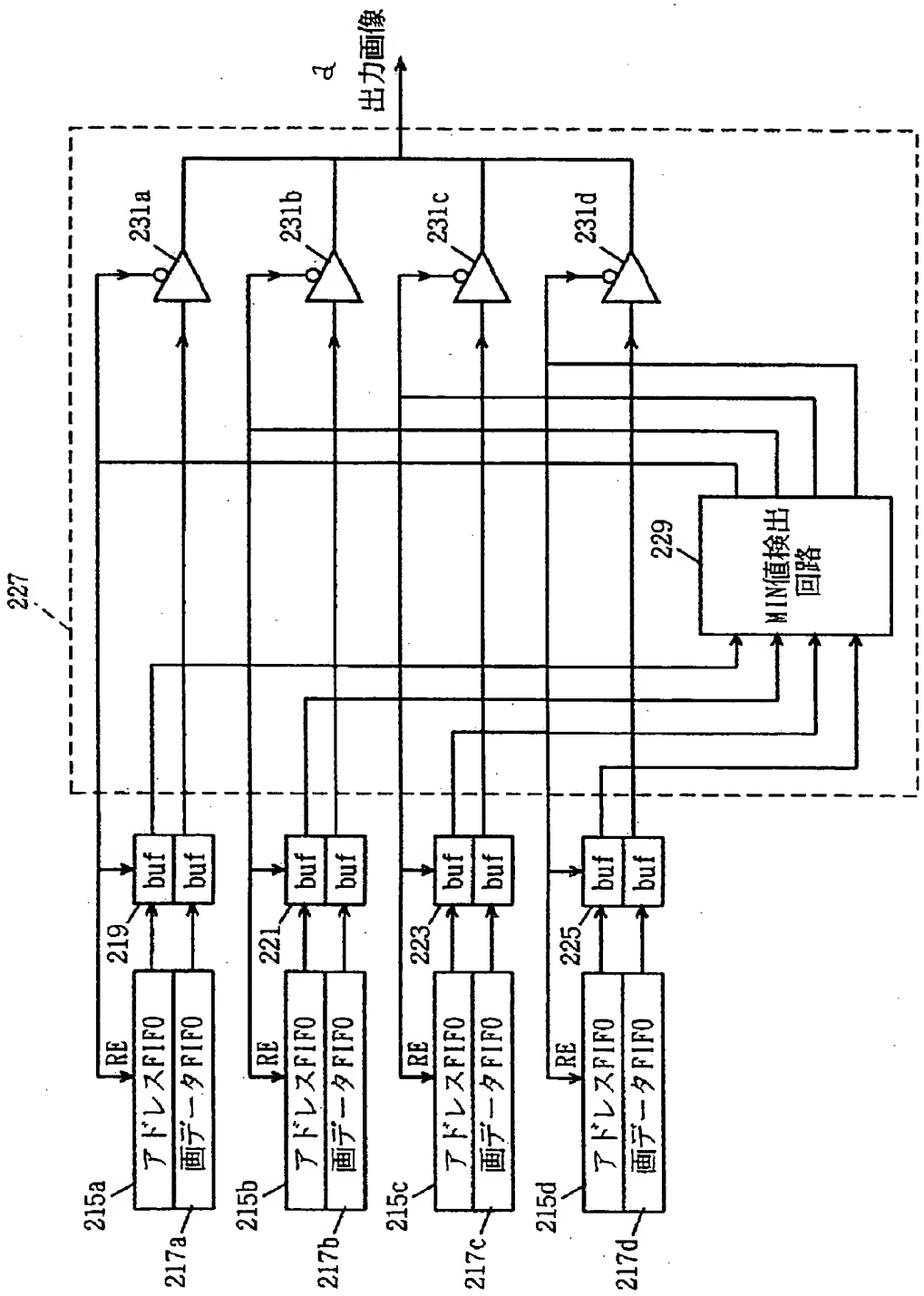
【図５】 Fig. 5 (see attached sheet)



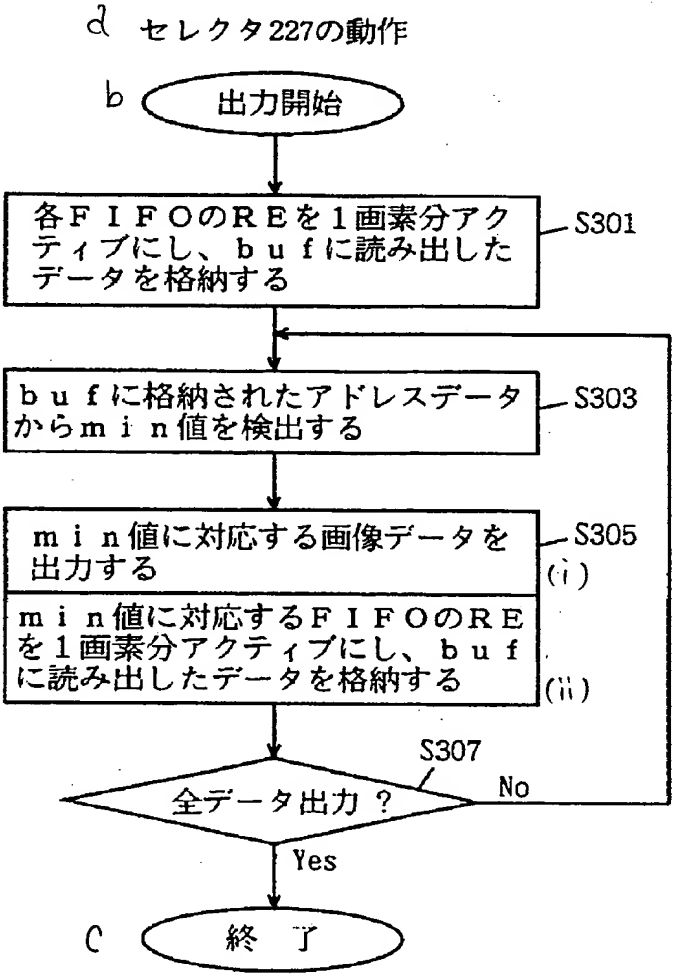
【図６】 Fig. 6 (see attached sheet)



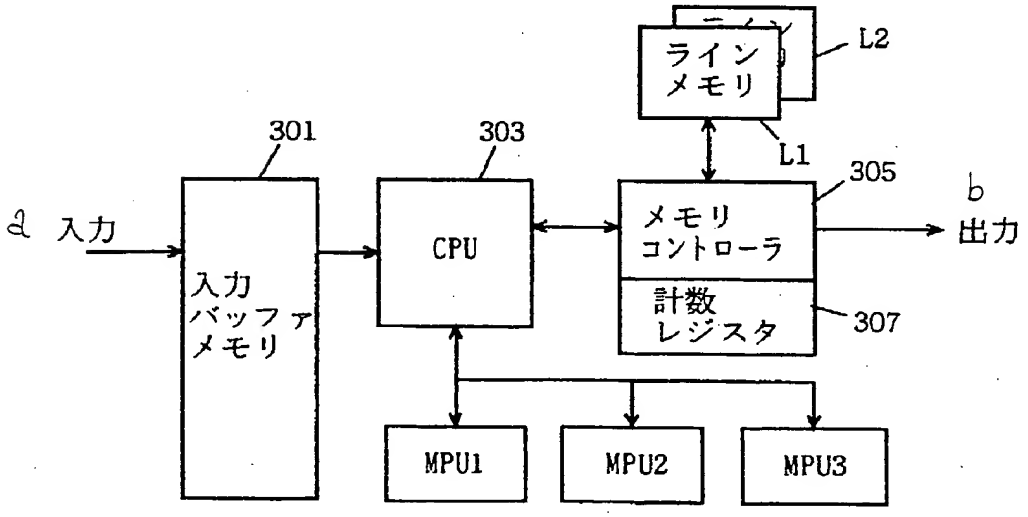
【図７】 Fig. 7 (see attached sheet)



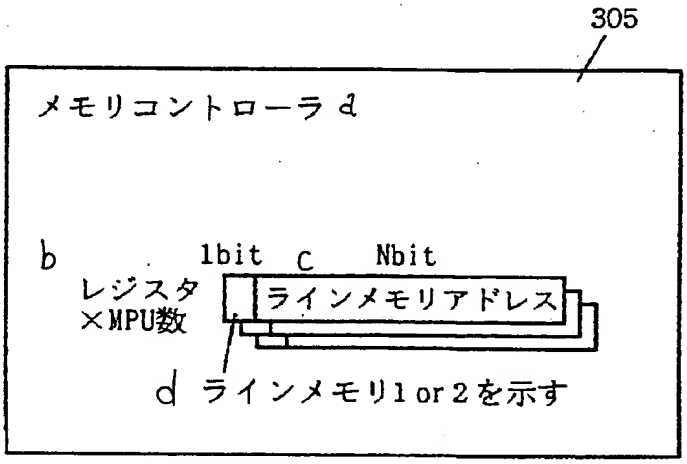
【図８】 Fig. 8 (see attached sheet)



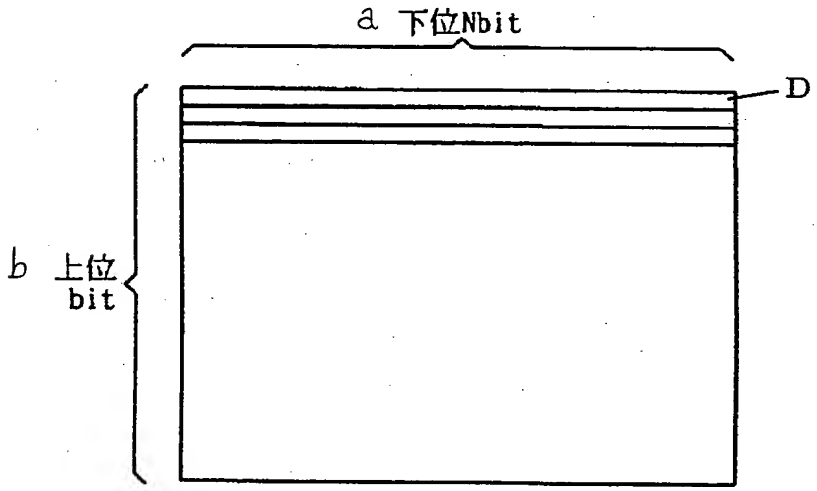
【図９】 Fig. 9 (see attached sheet)



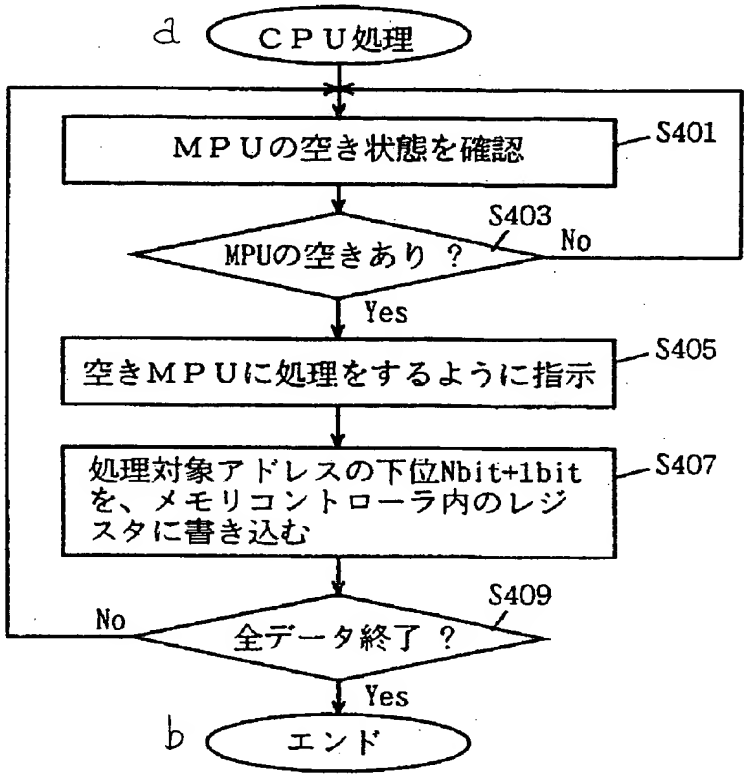
【図１０】 Fig. 10 (see attached sheet)



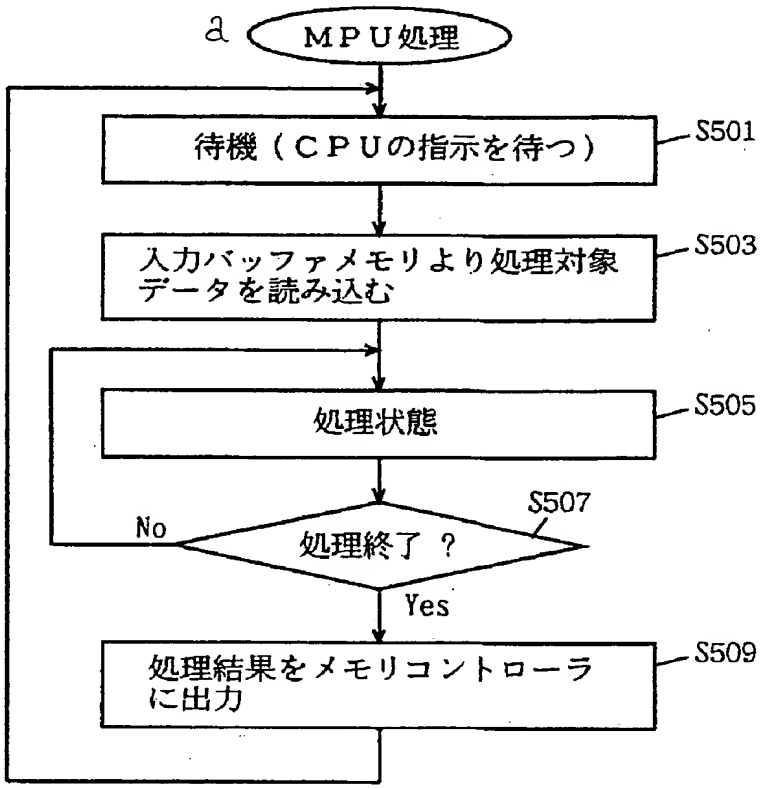
【図１１】 Fig. 11 (see attached sheet)



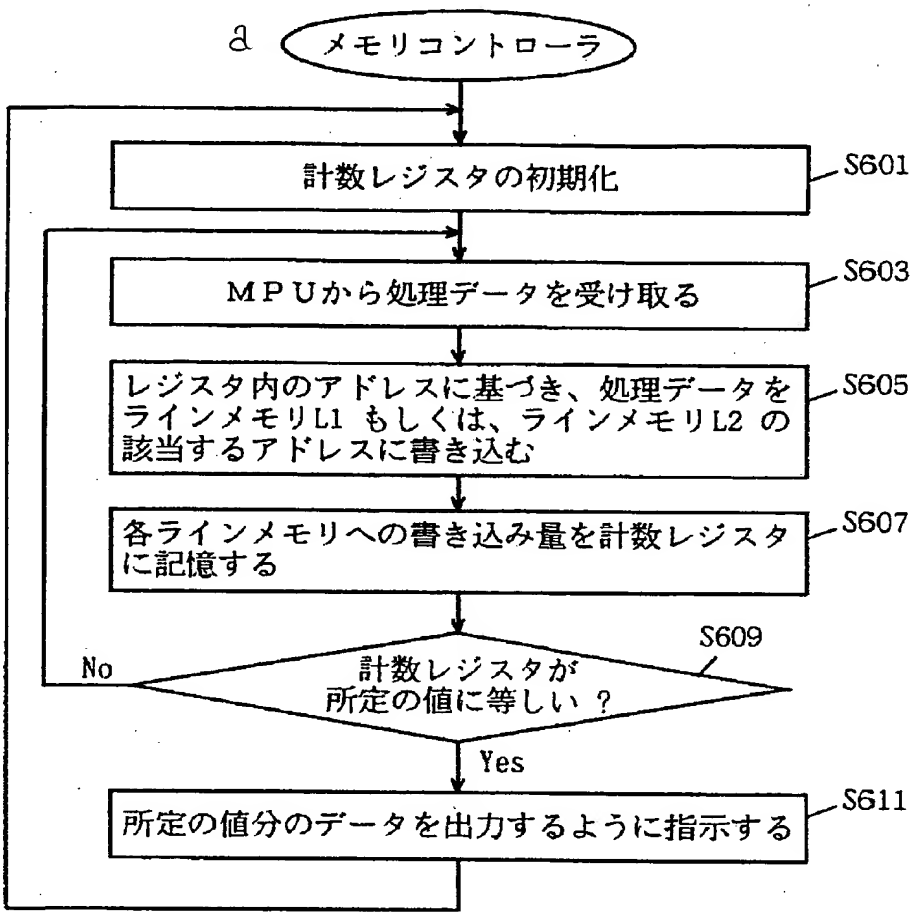
【図１２】Fig. 12 (see attached sheet)



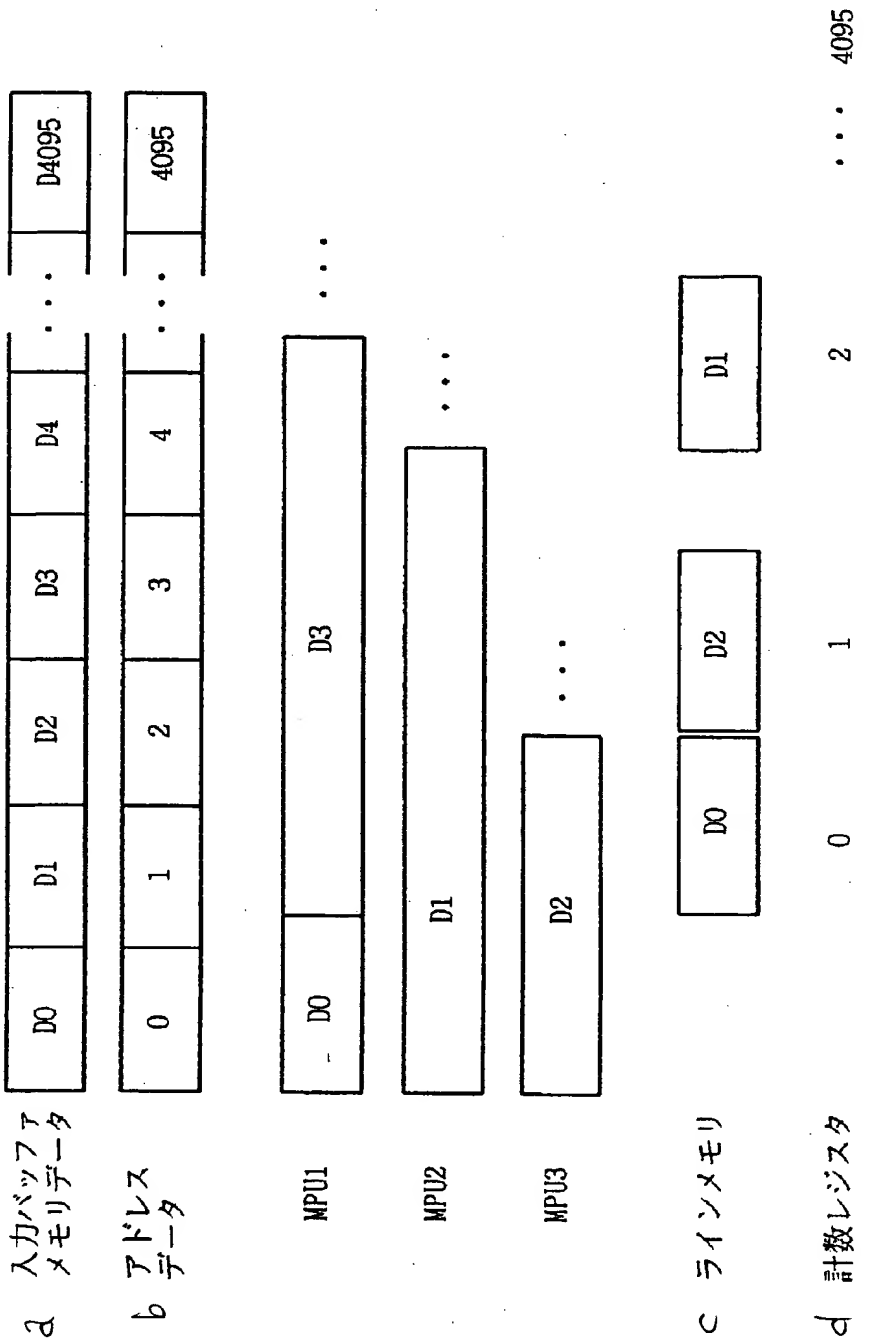
【図１３】 Fig. 13 (see attached sheet)



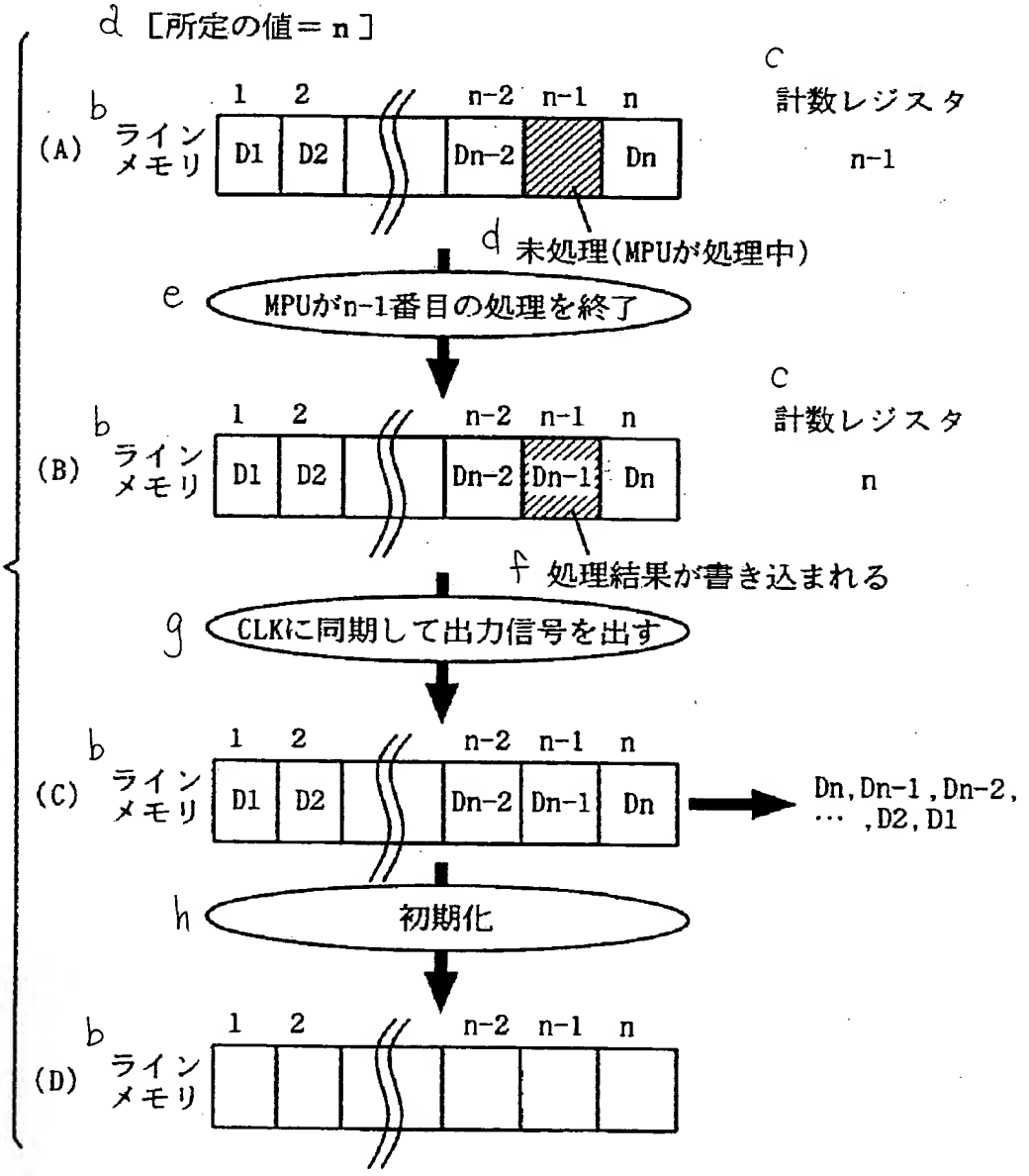
【図１４】 Fig. 14 (see attached sheet)



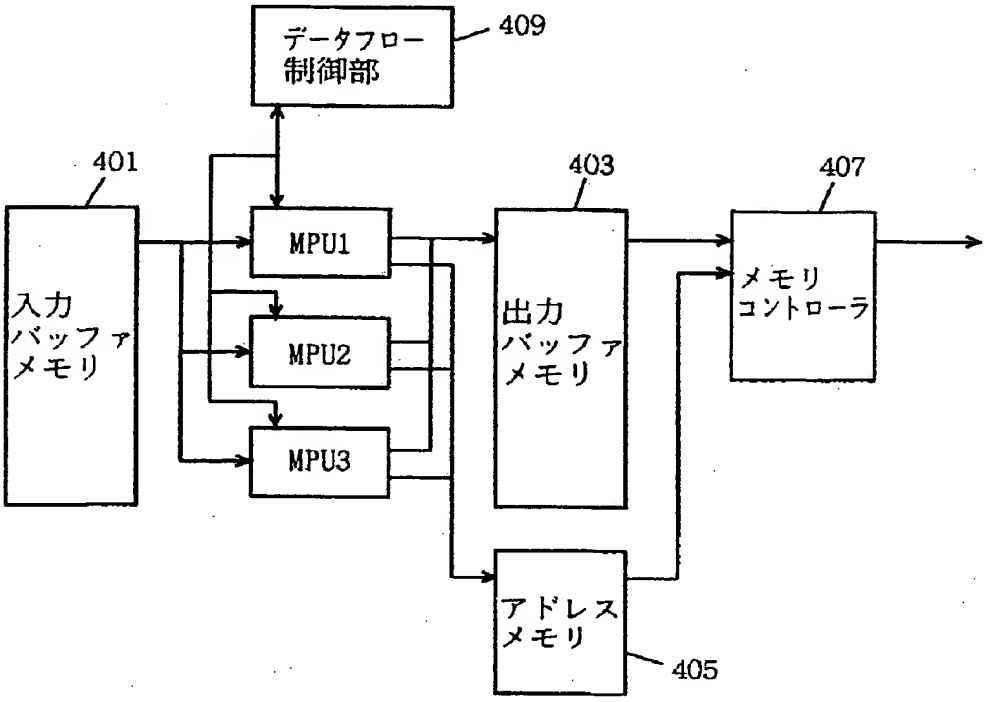
【図１５】 Fig. 15 (see attached sheet)



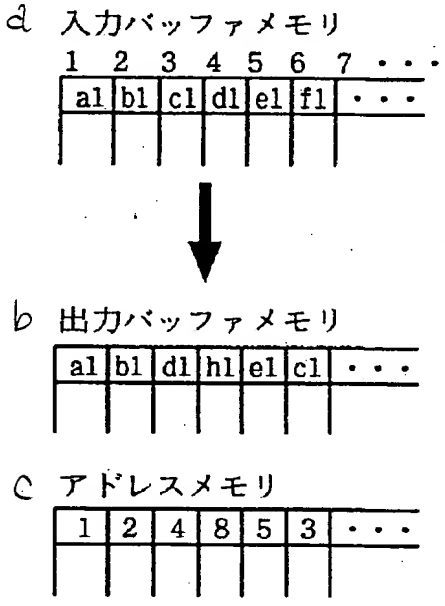
【図１６】 Fig. 16 (see attached sheet)



【図１７】 Fig. 17 (see attached sheet)

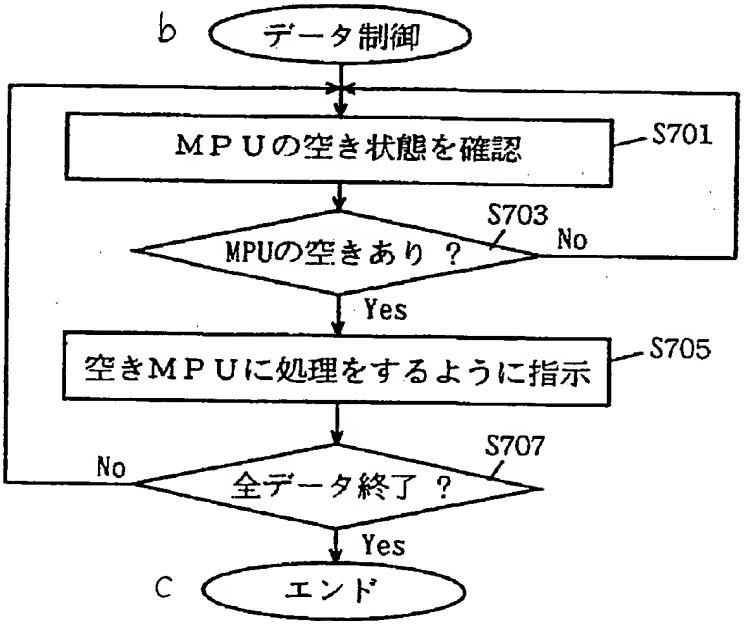


【図１８】 Fig. 18 (see attached sheet)



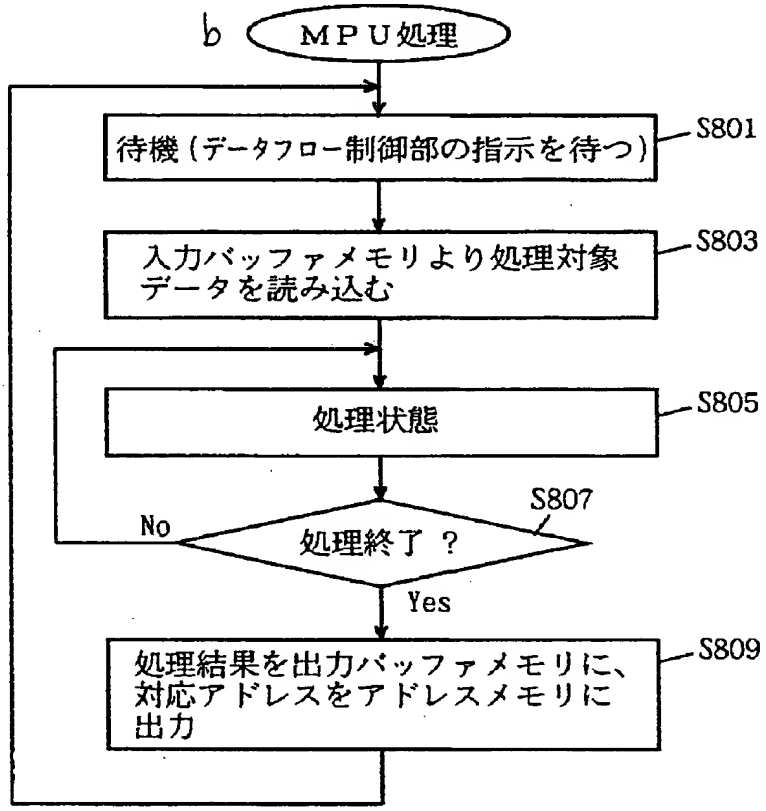
【図１９】 Fig. 19 (see attached sheet)

a ［データフロー制御部］

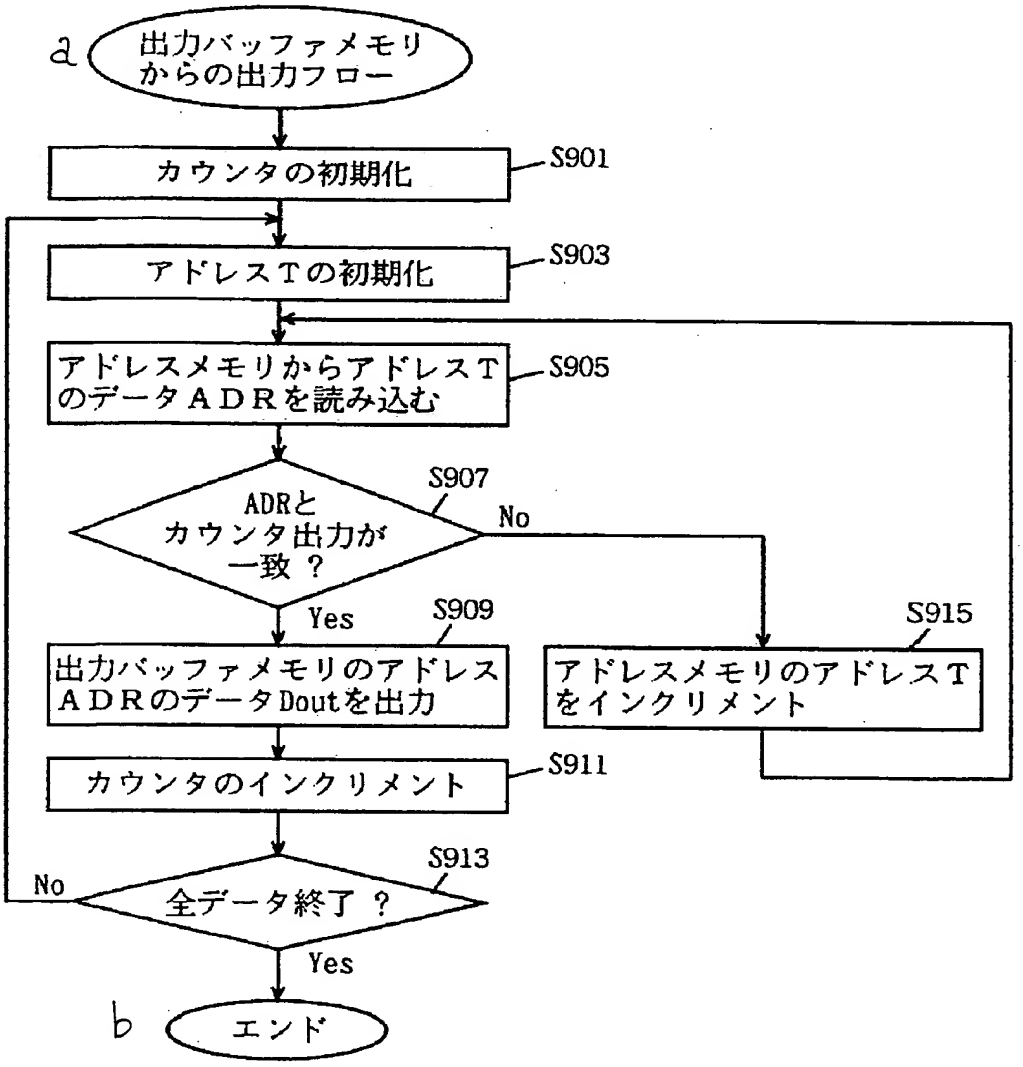


【図２０】 Fig. 20 (see attached sheet)

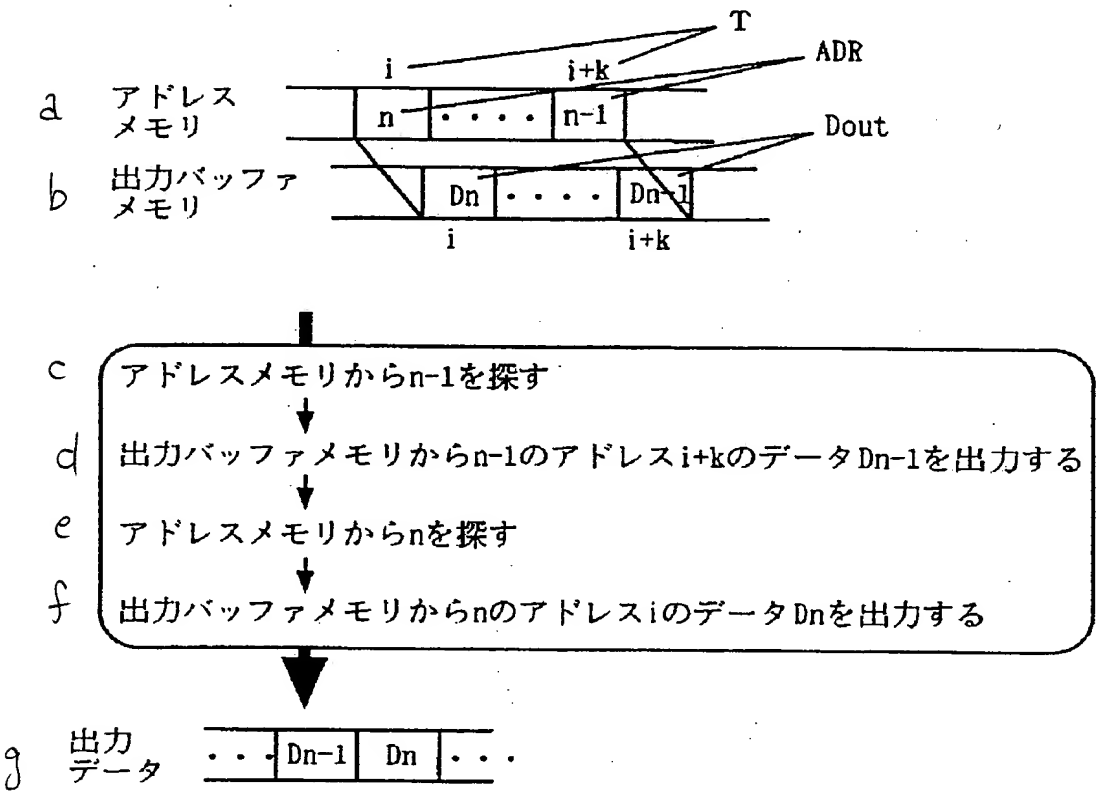
d［ＭＰＵ処理フロー］



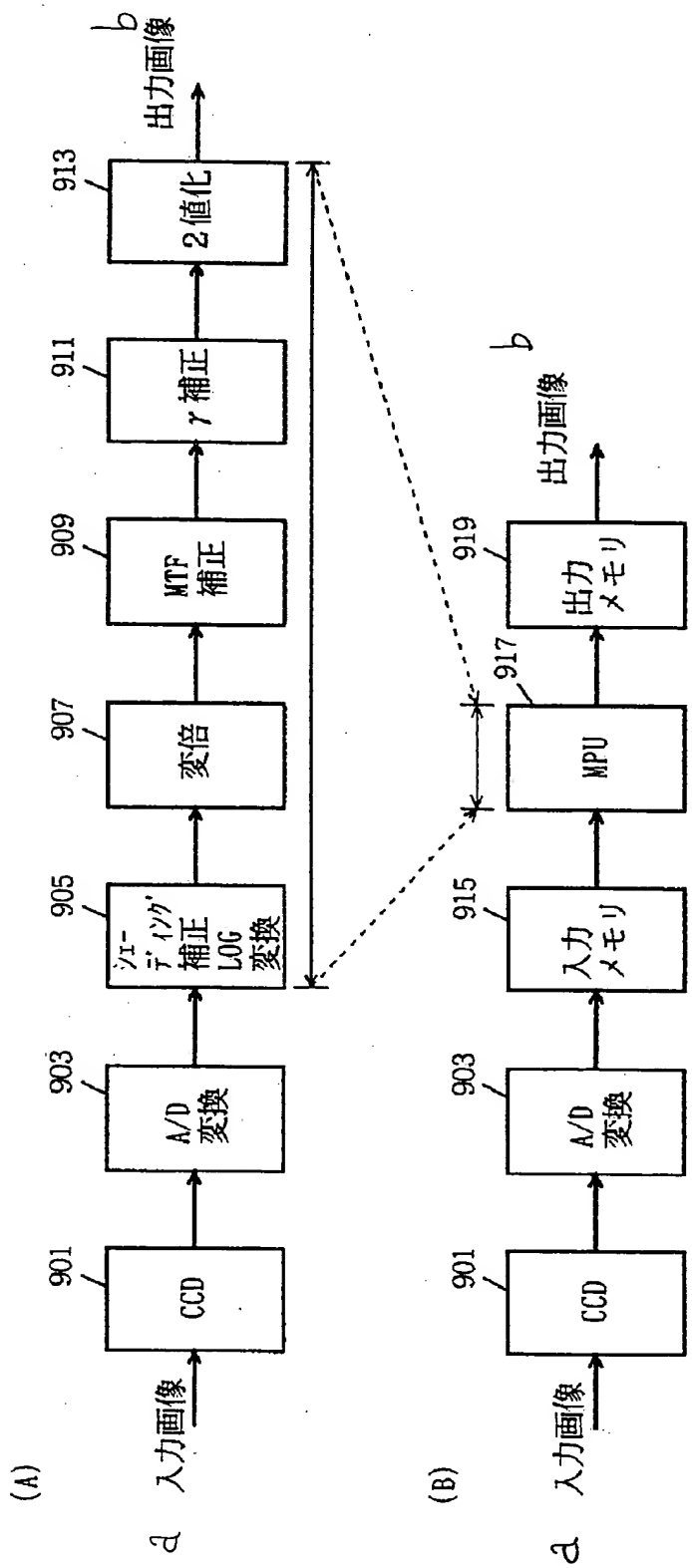
【図２１】Fig. 21 (see attached sheet)



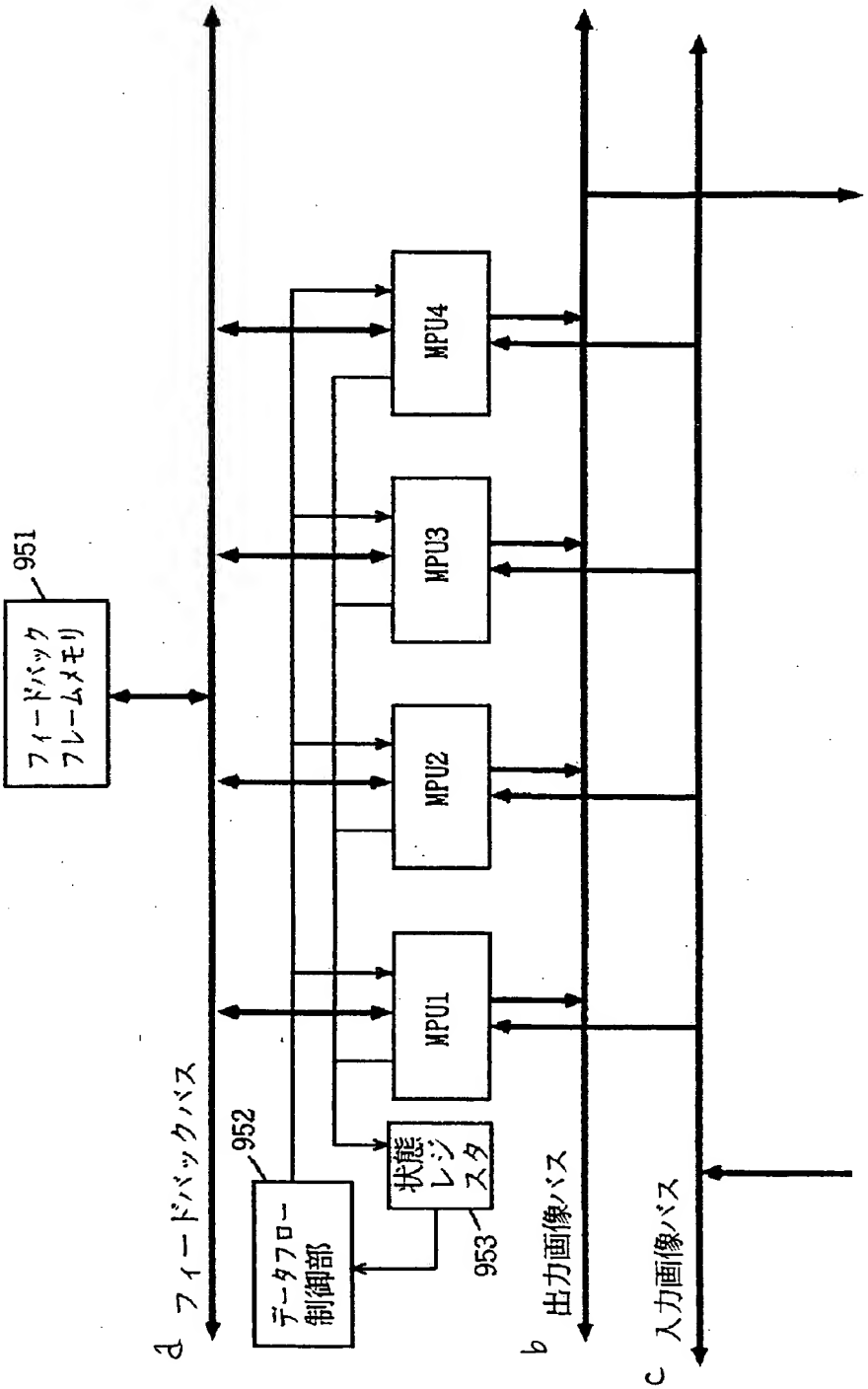
【図２２】 Fig. 22 (see attached sheet)



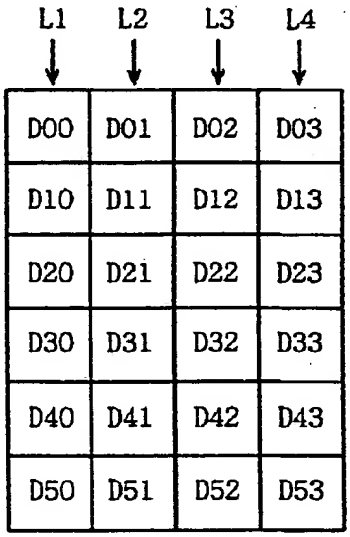
【図２３】 Fig. 23 (see attached sheet)



【図２４】 Fig. 24 (see attached sheet)



【図２５】 Fig. 25


L1
L2
L3
L4
D00
D01
D02
D03
D10
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D22
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D30
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D51
D52
D53

【図２６】 Fig. 26 (see attached sheet)